PATENT

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Rena Iov

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·Date

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Rena Iov

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants	:	Jason L. Fuller and Shaun D Compton
Application No.	:	10/081,624 Confirmation No.: 1950
Filed	:	February 20, 2002
For	:	MICROELECTRONIC DEVICE HAVING A PLURALITY OF STACKED DIES AND METHODS FOR MANUFACTURING SUCH MICROELECTRONIC ASSEMBLIES

Art Unit	:	1734
Docket No.	:	108298636US
Date	:	April 30, 2002

Office of Initial Patent Examination
Customer Service Center
Commissioner for Patents
Washington, DC 20231

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REQUEST FOR CORRECTED FILING RECEIPT

Sir:

Attached is a copy of the official Filing Receipt received from the U.S. Patent and Trademark Office in the above-identified application, for which issuance of a corrected Filing Receipt is respectfully requested.
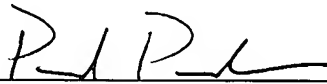
There is an error with respect to the following data, which was incorrectly entered:

Applicants(s): "Shaun D. Compton" should be –**Shaun D Compton**–.

The correction to be made has been marked in red on the enclosed copy of the Filing Receipt.

The correction is not due to any errors by applicants, and therefore no fee is due.

Respectfully submitted,
Perkins Coie LLP

Paul T. Parker
Registration No. 38,264

PTP:ri
Enclosures:
    Postcard
    Copy of Filing Receipt with Corrections

P.O. Box 1247
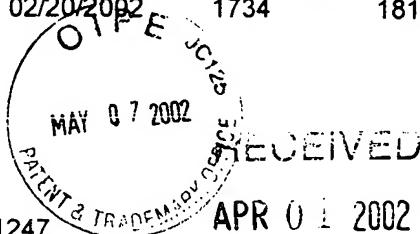Seattle, Washington 98111-1247
(206) 583-8888
Fax: (206) 583-8500

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/081,624	02/20/2002	1734	1814	108298636US	5	47	10

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PERKINS COIE LLP
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CONFIRMATION NO. 1950

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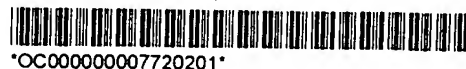
OC000000007720201

Date Mailed: 03/26/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)

Jason L. Fuller, Meridian, ID;
Shaun D. Compton, Boise, ID;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 03/26/2002

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

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Title

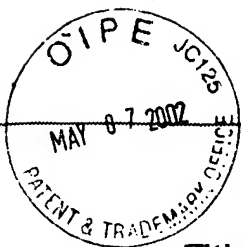MIcroelectronic device having a plurality of stacked dies and methods for manufacturing such microelectronic assemblies

Preliminary Class

156

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Title 37, Code of Federal Regulations, 5.11 & 5.15

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